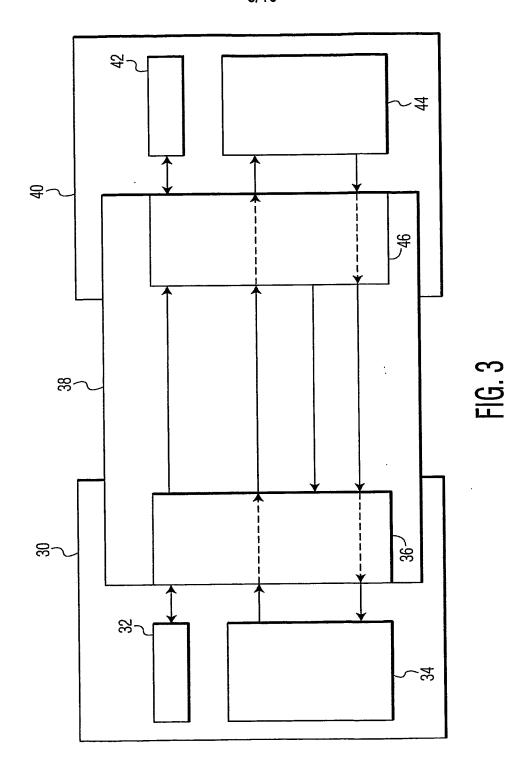


FIG. 2A PRIOR ART

	LD0	LD1	LD2	LD3	LD4	LD5	LD6	LD7		
			ST0	ST1	ST2	ST3	ST4	ST5	ST6	ST7
		P0	P1	P2	P3	P4	P5	P6	P7	
	0	1	2	3	4	5	6	7	8	9
,		\ \ \	<u> </u>	<u> </u>	<u> </u>	<u></u>	<u> </u>		†	1

FIG. 2B PRIOR ART



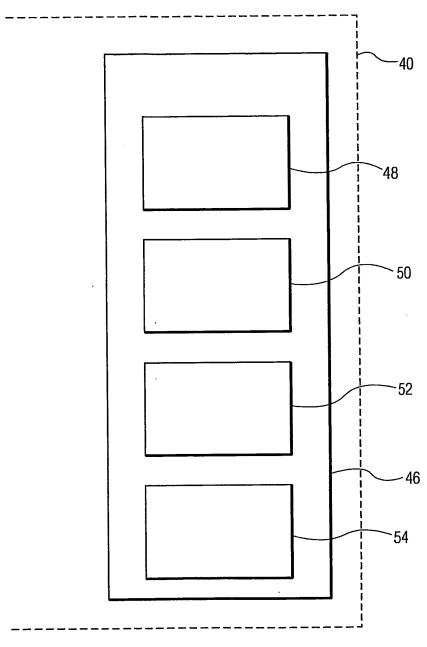


FIG. 4

OX00 OX04 OX04 OX04 OX08 OX08 OX08 OX0C OX0C OX10 OX10 - OX10 OX11 OX11 - OX14 OX14 OX18 OX18 OX10 OX10 OX20 OX20 OX20 OX20 OX24 OX24 OX24 OX24 OX3D OX3D OX3D OX3D OX3A OX3B OX3B OX3B OX3C OX40 OX40 OX40 OX44 OX48 OX48 OX48 OX48 OX48 OX48 OX48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X04 0X08 0X0C 0X0C 0X10 0X10 0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48	0.400	0,400		
0X08 0X0C 0X0C - 0X10 0X10 - - 0X14 0X14 0X18 - 0X1C 0X1C 0X1C - 0X20 0X20 0X20 - 0X24 0X24 - - 0X28 0X28 - - 0X3D 0X30 - - 0X34 0X34 - - 0X38 0X38 - - 0X40 0X40 - - 0X44 0X44 - - 0X48 0X48 - -				
0X0C 0X0C - 0X10 0X10 - 0X14 0X14 - 0X18 0X18 - 0X1C 0X1C - 0X20 0X20 - 0X24 0X24 - 0X28 0X28 - 0X2C 0X2C - 0X3D 0X30 - 0X34 0X34 - 0X38 0X38 - 0X40 0X40 - 0X44 0X44 - 0X48 0X48 -				
0X10 0X10 - 0X14 0X18 0X18 0X1C 0X1C 0X1C 0X20 0X20 0X24 0X24 0X24 0X28 0X2C 0X2C 0X3D 0X3A 0X3A 0X3A 0X3S 0X3S 0X3C 0X40 0X40 0X44 0X48 0X48 0X48				
0X14 0X14 0X18 0X18 0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X40 0X40 0X44 0X48 0X48			-	
0X18 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48	0X10	0X10	_	
0X18 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48				
0X1C 0X1C 0X20 0X20 0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X48 0X48	0X14	0X14		<u> </u>
0X20 0X24 0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X44 0X48 0X48	0X18	0X18		
0X24 0X24 0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X1C	0X1C		
0X28 0X28 0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X20	0X20		
0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X24	0X24		
0X2C 0X2C 0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48				
0X3D 0X30 0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X28	0X28		
0X34 0X34 0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X2C	0X2C		
0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X3D	0X30		
0X38 0X38 0X3C 0X3C 0X40 0X40 0X44 0X44 0X48 0X48	0X34	0X34		
0X40 0X40 0X44 0X44 0X48 0X48	0X38			
0X40 0X40 0X44 0X44 0X48 0X48				
0X40 0X40 0X44 0X44 0X48 0X48	0X3C	0X3C		
0X44 0X44 0X48 0X48				
0X48 0X48				
I 0X4C I I 0X4C I I	0X4C	0X4C		

FIG. 5A

	<u> </u>	~ /	~_		\
0X50		0X50			
0X54		0X54			
0X58		0X58		 <u>.</u>	
0X5C		0X5C		 	
0X6D		0X60			
0X64		0X64			
	-	0X68		 	
		0X6C		 ···	
	_	0X70		 	
		0X74		 	
	1				
-	-	0X78			
····		0X7C			
		0X80			
		0X84			

FIG. 5B

FIG. 6A

31:10			-	0x0*	
9:5	INT_ENABLE			0x0*	
				l	
4:0	INT_STATUS			0x0*	
ļ					
		L		<u></u>	

FIG. 6B

—Т					
1	į.]	}	1	
01.10				0x0*	
31:10		+			
9:8	1		1	0x0*	
	1	1		1	
l				ļ	
į					
				0.01	
7	RX_DMA_1	ļ	į	0x0*	
	RX_DMA_1 _status	1			
	}				
6	RX_DMA_0 _STATUS	1	\	0x0*	
l	STATUS	1		l	
1]				
5	TX_DMA_1 _STATUS		1	0x0*	j
	STATUS		Į.		
1					
4	TY DMA O			0x0*	
"	TX_DMA_0 _STATUS				
	_SINIO			l	
 	 				
 					
1	NEXT_TX_DMA	[0x0*	
1	1)]	į	
1		İ			
		1		-	
1	}		i i		
}		l		Ì	
1	1	1			
Ì		1			
l		Į.	`{		
	1		1		
	1	1			
1					
1	ļ	1			
	1				
				1	
-			1	1	
	+	=	1		

FIG. 6C

_		\sim		
0	NEXT_RX_DMA		0x0*	
				·
			!	
			i	
				,

FIG. 6C1

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1				
1		1	1	
ļ		 	0.01	
31:10	į į	 l —	0x0*	
01.10		 ļ		
9:0	MLBX_CODE	1	0x0*	
3.0	I MILDY COOL	 <u> </u>	0.00	

FIG. 6D

	ļ	S	ļ	
1				
		 		
31:10		 1 	0x0*	
31.10		 		
0.0	SCRCH_CODE		0x0*	
9:0	SOUCH PODE	i _	0.00	

FIG. 6E

			1	
		l —	0x0*	
			0.04	
MIRX CODEL			I UXU"	<u> </u>
_	MIRX CODE	MIRX CODE	MIRX CODE	0x0* MLBX_CODE 0x0*

FIG. 6F

Г						
1						
1	1		_			
31:	:10				0x0*	
				 -	2.04	
9	:0	SCRCH_CODE			0x0*	

FIG. 6G

31:10		— — 0x0*	
9		0x0*	
8	DMA_msb_first	0x0*	
7	RX_DMA_1 _int_enable	0x0*	
6	RX_DMA_0 _int_enable	0x0*	
5	TX_DMA_1 _INT_ENABLE	0x0 [,]	
4	TX_DMA_0 _int_enable	0x0'	
3	RX_DMA_1 _int_status	0x0	
2	RX_DMA_0 _int_status	0x0	
1	TX_DMA_1 _int_status	0x0	
0	TX_DMA_0 _int_status	0x0	

FIG. 6H

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31:26 — 0x0*
25:0 DMA_ADDR — 0x0*

FIG. 6I



31:26			0x0*	
25:0	DMA_ADDR	 •	0x0*	

FIG. 6K

31:16			0x0*	
15:0	DMA_SIZE	-	0x0*	

FIG. 6L

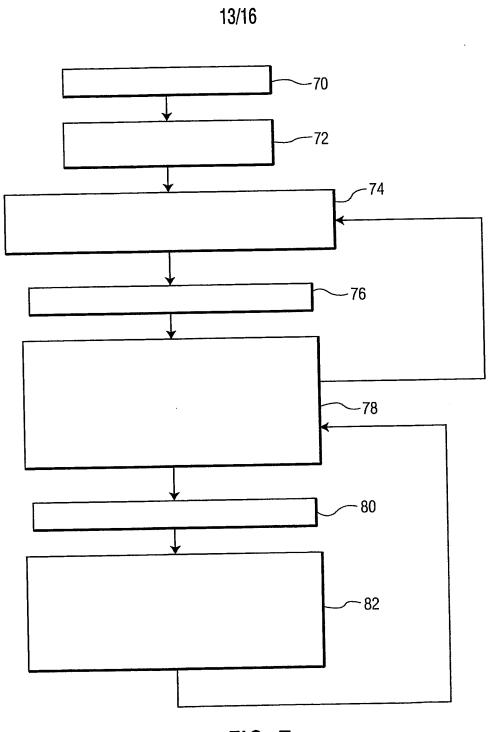


FIG. 7

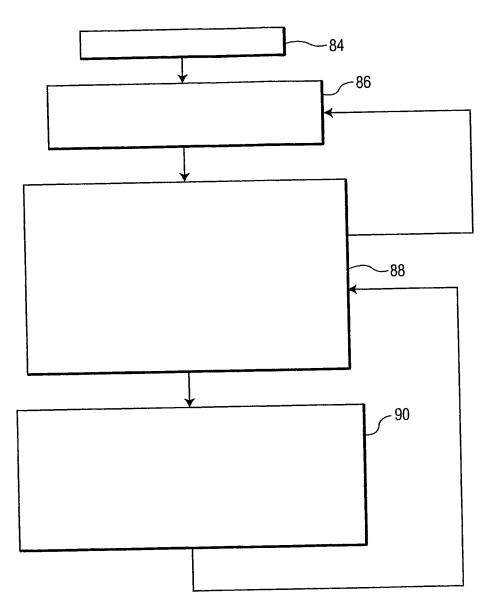


FIG. 8

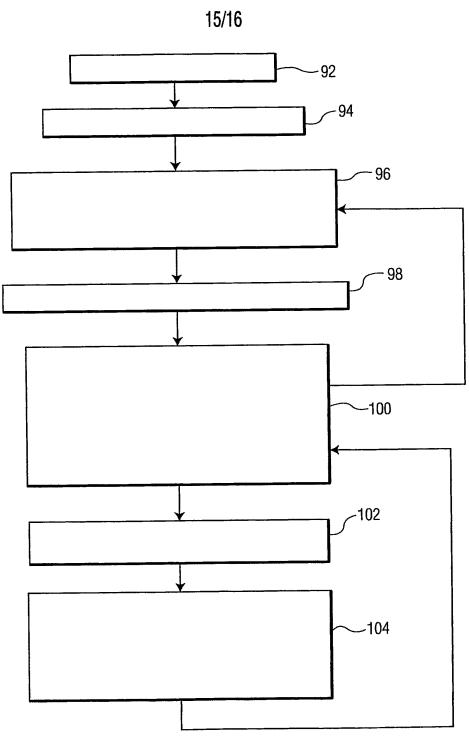


FIG. 9

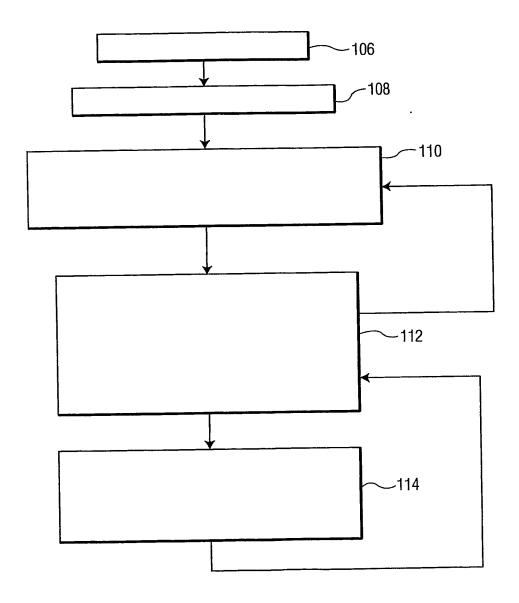


FIG. 10